

Course Description:

ELEC204 is a course targeting electrical and computer engineering students to build the fundamental digital design and experimentation concepts. In this course we will study the combinational logic circuit design techniques, sequential logic circuits, registers and counters, memory, and state machines. In parallel to lectures we will develop experimental skills studying various digital design techniques during our laboratory hours. Our experimental setup will be based on Field Programmable Gate Array (FPGA) structure, and we'll experience schematic and hardware description language (HDL) design tools.

Grading

Elec 204 is a 4-credit course with the lecture part having 3 credits and the lab work part having 1 credit. The lecture and lab work parts of the Elec 204 will be graded separately. **A passing final letter grade requires passing grades from both the lecture and lab work parts.** The lecture and lab work letter grades will be combined (3 credits for the lecture and 1 credit for the lab work) for the final letter grade.

Lecture part: In class quizzes (20%), midterm exam (35%), final exam (45%).

Lab work part: Bi-weekly lab assignments (50%), lab midterm (20%), lab project (30%).

Academic Dishonesty

The students taking this course are expected to submit their own work in all midterms, projects, homeworks and in class quizzes. Please be aware that, being a part of a dishonest plot intentionally (for example, helping others cheat, doing others' homeworks or lab work, giving your reports to others) will also be considered as cheating. Disciplinary acts will be taken for the students who are involved in academic dishonesties, and they will not be offered any voluntary compensation.

Lecture Regulations

- **Students should attend all lectures. There will not be any form of make-ups for in-class quizzes.**
- **Students who are late more than 15 minutes will not take in-class quizzes.**
- **Disruptive talking, cell phone use, walking in&out during the lecture, eating&drinking and any kind of distraction to the lecture will not be allowed during the lecture.**